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**AMENDMENT TRANSMITTAL LETTER (Large Entity)**

Applicant(s): Jun Kanamori

Docket No.

IIZ.008D

Serial.No.

09/398,189

Filing Date

September 17, 1999

Examiner

S. Rao

Group Art Unit

2814

**METHOD OF FABRICATING A SEMICONDUCTOR DEVICE WITH SELF-ALIGNED SILICIDE AREAS FORMED USING A SUPPLEMENTAL SILICON OVERLAY**

TO THE COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

**CLAIMS AS AMENDED**

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	18 -	20 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	2 -	4 =	0 x	\$86.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00

- ☒ No additional fee is required for amendment.
- ☐ Please charge Deposit Account No. \_\_\_\_\_ in the amount of \_\_\_\_\_
- ☐ A check in the amount of \_\_\_\_\_ to cover the filing fee is enclosed.
- ☒ The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0238
- ☒ Any additional filing fees required under 37 C.F.R. 1.16.
- ☒ Any patent application processing fees under 37 CFR 1.17.

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Signature

Dated: December 29, 2003

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I certify that this document and fee is being deposited on \_\_\_\_\_ with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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